

Appl. No. 10/604,268
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3. Replacement Abstract:

A method and related apparatus for utilizing an ACPI to maintain data stored in a DRAM
~~maintaining stored data in a dynamic random access memory~~ includes a processor, a
5 DRAM ~~dynamic random access memory~~ (DRAM), a south bridge chipset, and a
rechargeable battery device. The south bridge chipset includes a system controller, a
buffer, a memory controller, an integrated device electronics controller, and a data
conversion circuit. The data conversion circuit converts a hard-disk access command
transmitted from the system controller into a memory access command of the memory
10 controller. The memory controller accesses the buffer and the DRAM by executing [[a]]
the memory access command. When the computer system enters a power-saving mode, a
switch is turned on allowing the battery device to constantly self-refresh the DRAM for
maintaining the data stored in the DRAM. When the computer system powers up, the
switch is turned off and the battery device is re-charged.

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